



**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q65687

Franck GREVERIE, et al.

Appln. No.: 09/941,745

Group Art Unit: 3662

Confirmation No.: 7387

Examiner: Not assigned

Filed: August 30, 2001

For: A CONTROL METHOD AND CIRCUIT FOR USING A HETEROJUNCTION  
BIPOLAR TRANSISTOR POWER AMPLIFIER IN A ZERO INTERMEDIATE  
FREQUENCY ARCHITECTURE TRANSMITTER

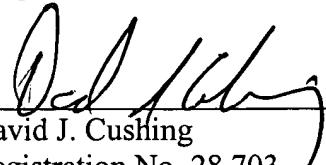
**STATUS INQUIRY**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The undersigned respectfully requests to be advised of the status of the above-identified application. The application was filed August 30, 2001, and Applicants still await a first Office Action.

Respectfully submitted,

  
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Date: January 4, 2005